

19 **V**

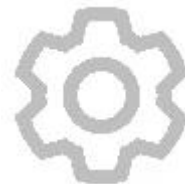


20 □□□□□□ □□ □□□□

□□□□ □□□□ □□ □□□□□□□□□□□□□□
 □□□□□□□□ □□□□□
 □□□□□□ □□□□ 30 □□ □□□□□ □□□□



80% ██████████ ██████████ ██████████

[illegible]

□□□□ □□□□

00000 00000 000000 000000
 0000000 00000000 00000000
 0000 00 00000000 0000



□ □ □ □ □ □ □ □ □ □

၀၀၀၀ ၀၀၀၀၀၀ ၀၀၀၀၀ ၀၀ ၀၀ ၀၀၀၀၀၀
 ၀၀၀၀၀၀၀၀
 ၀၀၀၀ ၀၀၀၀၀ ၀၀၀၀၀ ၀၀၀၀ ၀၀
 ၀၀၀၀၀၀၀၀ ၀၀၀၀၀၀၀၀၀ ၀၀၀ ၀၀၀၀၀၀၀
 ၀၀၀၀၀၀၀ ၀၀၀၀၀ 9 ၀၀၀၀



11111111

000000 000 0000000 000000000 00 00000
 000000000 00 0000000 000 000
 000 00000000 000000000 NEST
 France.
 00 00 00000 00 00000000 00000000 00000
 000000



□□□□□ □□□□□□□□ □□□□□

በጣም በጣም በጣም በጣም በጣም በጣም
በጣም በጣም በጣም በጣም በጣም በጣም
በጣም በጣም በጣም በጣም በጣም በጣም

[illegible]

V g

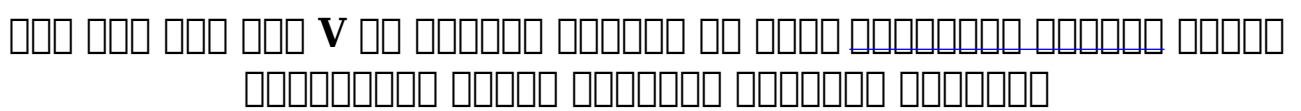
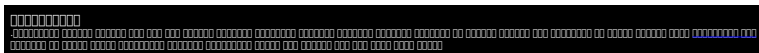




Diagram illustrating a 16-bit bus structure. The bus is divided into two sections: the first 8 bits are labeled 'Data' and the next 8 bits are labeled 'Address'. A blue line connects the first 8 bits to the next 8 bits, indicating a data path. Below the bus, there are two groups of blocks: 'Data' and 'Address'.



☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐ ☐

□□□□□□ □□□□/□□□□□□/□□□□□□/□□□□□□/□□□□□□/□□□□□□ □□□□

